

### **AMENDMENTS TO THE SPECIFICATION:**

Please replace the paragraph beginning at page 10, line 23 in the specification with the following replacement paragraph:

— This will ~~i.e.~~ inter alia reduce processing time and thus represent a significant advantage as the interleaving process is already by itself relatively time consuming: the whole block of data to be interleaved is in fact to be made available before the interleaving process proper is started since the last input data to the process could be the first output data therefrom. —